



PATENT APPLICATION

#11
formal
drawings
11/20/02
asj

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Ichia YUDASAKA et al.

Group Art Unit: 2826

Application No.: 09/936,040

Examiner: A. Sefer

Filed: December 11, 2001

Docket No.: 110554

For: A THIN FILM TRANSISTOR AND A METHOD FOR MANUFACTURING
THEREOF

REQUEST FOR APPROVAL OF DRAWING CORRECTIONS

Director of the U.S. Patent and Trademark Office
Washington, D.C. 20231

Sir:

The Examiner is requested to review and approve the proposed correction to Figures 13, 14(A)-(E) and 16(A)-(E), marked in red on the attached copy of such drawing figures.

Upon approval by the Examiner, and upon allowance of this application, the formal drawings will be corrected.

Respectfully submitted,

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